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## Pixel detector developments

*Friday, 2 October 2009 16:15 (20 minutes)*

The development of pixel detector applications developed in CP3 using different technologies will be presented.

The first part of the talk will include a review of the different technologies available for pixel detectors.

The second part will introduce the SOI pixel project developed in CP3 using 2um DICE-UCL technology.

The last part of the talk will introduce NA62 EOC read-out chip design, done using 130nm technology from IBM.

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