JUNO workshops in Brussels : large PMT electronics, small PMT system and Light Concentrators Proposal

Monday, 14 November 2016

LPMT session - Jean Sacton Seminar Room - 1G-003 (09:15 - 10:50)

time [id] title	presenter
09:15 [1] Welcome	CLERBAUX, Barbara Mr YANG, Yifan
09:20 [2] Introduction	STAHL, Achim JIANG, Xiaoshan
09:30 [3] HV unit	OLSHEVSKIY, Alexander
09:50 [4] Vulcan-chip : (oral) status report	
10:10 [5] Tsinghua ADC and package	WEI, Wei
10:30 [7] Power-board	STEINMANN, Jochen

LPMT session - Jean Sacton Seminar Room - 1G-003 (11:10 - 13:00)

time [id] title	presenter
11:10 [8] progress report of cables and connectors	SUN, Yunhua
11:40 [6] GCU	BELLATO, Marco
12:10 [48] Interface GCU-HV Discussion	

LPMT session - Jean Sacton Seminar Room - 1G-003 (14:00 - 16:00)

time [id] title	presenter
14:00 [13] status and issues about potting electronics on PMT	QIN, Zhonghua
14:40 [46] PMT Base and Receiver Interface	GREWING, Christian
15:20 [47] Interface with electronics and HV from PMT bases	WANG, Zhimin

LPMT session - Jean Sacton Seminar Room - 1G-003 (16:20 - 19:40)

time [id] title	presenter
16:20 [15] FEC and front-Dend protec0on progress	YAN, Xiongbo
17:00 [18] Discussion : towards the first prototype	

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LPMT session - Jean Sacton Seminar Room - 1G-003 (09:00 - 10:50)

time [id] title	presenter
09:00 [19] Reliability : introduction	STAHL, Achim
09:10 [49] Reliability How-To	STEINMANN, Jochen
09:30 [20] Schedule and reliability	JIANG, Xiaoshan
10:20 [22] JUNO Electronics Reliability	NING, Zhe

LPMT session - Jean Sacton Seminar Room - 1G-003 (11:10 - 12:45)

time [id] title	presenter
11:10 [33] Unexpected events : generation of a list of events to be considered	
11:40 [32] Early failures and tests	STEINMANN, Jochen
12:00 [34] GCU Production tests	DAL CORSO, Flavio
12:15 [50] A setup to test multiple GCUs with physics events	GARFAGNINI, Alberto
12:30 [9] Back-end cards	YANG, Yifan

LPMT session - Jean Sacton Seminar Room - 1G-003 (15:00 - 16:00)

time [id] title	presenter
15:00 [28] Proposal for integrating the network switch function into BEC	ZHANG, Jie
15:20 [51] Back-of-the-envelop trigger calculations	LIPPI, Ivano
15:30 [52] Few introductory remarks on GCU synchronisation	LIPPI, Ivano
15:40 [11] Trigger/timing progress and status	GONG, Guanghua

LPMT session - Jean Sacton Seminar Room - 1G-003 (16:20 - 18:00)

time [id] title	presenter
16:20 [29] sheduling the first and second prototypes	
16:50 [30] AOB - Discussion and conclusions	