The Askaryan Radio Array

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Outline

- ARA
 - Motivation/goal
 - Detector geometry
 - Event reconstruction
 - The test bed
 - Power supplies
- Current IIHE involvement
 - Digital data transmission system

The motivation/goal

 Expecting <1 GZK neutrino interaction per km³ per year
 Big detector needed





Want to detect neutrinos based on:

- Emission of coherent radio waves from neutrino-induced EM-cascades (predicted by Askar'yan, 1962)
- Verified at SLAC in 2000

Why this detection method:

- Radio waves have high attenuation length in certain media (ex.: ~800m in ice)
- Radio antennas are cheap sensors compared to optical modules

Detector geometry

Askaryan Radio Array



Detector geometry



Event reconstruction



The test bed



IIHE - 19/04/2011

Power supplies



IIHE - 19/04/2011

30

29

27

28

Day

1.33 km

tation Area

IIHE work

The IIHE involvement

Long distant wireless network
 GPS, Rubidium clock tests
 both together with

Chiba university

3. Digital data and clock transmission for antenna data

The challenge:

- In general:
 - data and clock transmission:
 - at highest speed
 - lowest power consumption possible
 - over a distance of >200m
 - Precise timing between holes
- Specifically:
 - Speed: O(100Mbits/s)
 - Transmission distance: 250m
 - Time precision in the hole: <50 ps











The motivation

Collaboration status in August 2010:

- analog data transmission via copper cable
- But there are disadvantages:
 - EMI problems
 - Bulky cables
 - Problems with timing calibration (high jitter)
 - Too much power needed for transmission

Proposed alternatives:

- Digital over optic fiber -> rejected at that time because of high costs and fragility
- Digital over copper:
 - Only small cable needed
 - Low power consumption possible

Developed in Brussels

The setup



The setup Clock transfer system



Cable driver:

- Amplifying the incoming clock to transfer it
 - 400 ps rise time, 25 ps output jitter
 - 1 Vpp output
 - Power consumption: 520 mW

Cable equalizer:

- equalization
- DC restoration
 - 750 mVpp output
 - Power consumption: 255 mW

Clock conditioner:

- Loop filtering
- Jitter cleaning
- Clock distribution
 - 200 fs output jitter
 - Power consumption: 578 mW

The setup Clock conditioner



Test results



| | Description | Mean | Std Dev | Number of samples |
|----------------------|---------------------------------|---------------|---------|-------------------|
| Г | Period1, Ch1 | 50ns | 16ps | 239988 |
| Precision of period | Period2, Ch2 | 50ns | 27ps | 239976 |
| | TIE1, Ch1 | Os | 26ps | 240000 |
| | TIE2, Ch2 | Os | 50ps | 239988 |
| shift between clocks | Skew1, Ch1, Ch2 | -8ns | 52ps | 239988 |
| | IP-failure rate (down to -40°C) | 1.2E-8/32bits | | 4.3E11 (72 hours) |
| | Power consumption | ~2.3W | | |

Future plans

Presented system was not accepted by the ARA collaboration: Trigger rate raised from ~4MHz to ~40MHz Copper prizes raised (well shielded cable is very expensive) Power consumption got less important RF over fiber is the current choice Digital system might still be interesting for other experiments

Future plan:

Digital over optic fiber:

- One fiber cable for both directions carrying data and clock
- Speed: ~2GBits/s
- Clock recovered from the data (CDR clock data recovery)
- Power consumption <700mW (for one system)

Summary

- ARA:
 - ARA detector will be able to efficiently detect GZK neutrinos
 - Test bed and power supplies are deployed to investigate possibilities
 - Future: Two ARA-stations will be ready this summer, to be deployed in the coming two years
- IIHE group
 - Currently involved in Wireless communication, GPS, rubidium clock
 - Digital data transmission system was assembled and successfully tested at the IIHE
- Future plans:
 - Digital data transmission via optic fiber
 - Firmware design for ARA electronics

Backup





Hsin-Yi Tu

The setup Data transfer system

• TLK100

- 10/100 Mbits/s Ethernet transceiver for a 25MHz input clock
- In current design driven by 20MHz clock
 - > 8/80 Mbits/s
- Power consumption: 200 mW





Station electronics



Outline

- The setup
 - Data transfer system
 - Clock transfer system
- Operation, tests & results
 - Clock precision
 - Power consumption
 - IP-failure rate
 - Behaviour for low temperatures