Challenges and opportunities for a First Level Trigger based on Silicon Trackers for the High Luminosity LHC



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## Run1 legacy



#### Weight and the set of the set









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CMS Exotica Physics Group Summary – March, 2014

njet=0

njet=0

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## What could be done next?

#### Run 2 (2015-18):

#### Gain depends on coupling (qq, gg, qg)

I year worth ~250 years of 8 TeV data for e.g. 4 TeV Z' or 2 TeV squarks big gains in sensitivity



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~3000 fb<sup>-1</sup> expected in 10 years running

Excellent opportunity to search for (rare) and new phenomena

Need anyway still to trigger on "SM" objects (leptons, b, jets, MET)

## Implications for the Trigger



- Sector States and the sector of the sector o
  - $\bigcirc$  About 6k primary tracks per bunch crossing in the Tracker volume  $|\eta| < 2.5 \dots$ 
    - $\odot$  ...plus any other coming from  $\gamma$  conversions and nuclear interactions
  - <u>~ one order of magnitude larger wrt LHC</u>
  - Severe Triggering conditions
    - Too many primary vertices, need to have smarter triggers combining information from several subdetectors
    - Need to maintain low thresholds for basic objects, even with an increase in the L1-Accept bandwidth (currently at 100 kHz)
  - Both ATLAS and CMS will replace their "inner trackers" to cope with the nasty environmental conditions
    - The usage of the Tracker would help to disentangle among those 140 pileup events





## Why a Track Trigger at L1



- HL-LHC physics goals require excellent Trigger selectivity on basic objects (leptons, jets, taus, b-jets, MET)
  - This might be jeopardized by the increased level of pileup events (140 on average)
    - Huge rate of  $\mu$  from heavy flavors  $\Rightarrow$  use better p<sub>T</sub> resolution from tracker
    - Prompt electrons at L1 need to be separated from huge  $\gamma \Rightarrow$  Tracker tracks • High E<sub>T</sub> jets from (many) different primary vertices  $\Rightarrow$  jet-vertex association
    - •Photon isolation in Calorimeters compromised by large pileup  $\Rightarrow$  use tracks





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## The challenge and the way out

rate (in MHz/cm

Cluster I

#### Take data off the tracker

- $\bigcirc$  ~ 4k primary tracks within  $|\eta| < 2.5$ 
  - Large data rates (up to 25 MHz/cm<sup>2</sup>)
    - huge contribution from nuclear interactions and photon conversions
  - ~1.3 events/mm × Gauss( $\sigma$ =4 cm)
  - Short L1A trigger latencies (10-20  $\mu$ s)
  - Cannot read all (~60 M strips) channels at 40 MHz
    - Even a 1% occupancy: 0.5 M channels x 40 MHz x 20 bit = 400 Tb/s

~120k links at 3.25 Gb/s (GBT) - Current CMS Tracker has 40k links (320 Mb/s)

Need to

suppress hits from low  $p_{\mathsf{T}}$  tracks read at smaller (affordable) rate

# Once data are off-detector, find tracks and

formidable pattern recognition problem

• need latencies of ~5  $\mu s$ 

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## Trigger architectures

#### PUSH path (CMS)

Reduced Tracker information readout at 40 MHz and then combined with calorimeter & muon at L1

Trigger objects made from tracking, calorimeter & muon inside a Global Trigger module

#### PULL path (ATLAS)

Use calorimeter & muon detectors to produce a "Level-0" to request tracking information in specific regions

Tracker sends out information from regions of interest to form a new combined L1 trigger



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### Data reduction



### Track Trigger with pull architecture (ATLAS)





#### The L0+L1 scheme

Level-0:

- Coarse calo and muon data
- Rate 40 MHz → 500 kHz
- Latency < 6.4  $\mu$ s
- Defines Region of Interest (ROIs) for L1
- Level-1:
  - Tracker data only from ROIs
  - Refined information from calo and muons
  - Rate 500 kHz →200 kHz
  - Latency < 20  $\mu$ s





Solution Need to provide data from the FE in less than  $\sim 5\mu$ s.

# Select only hits from "high-p<sub>T</sub>" tracks

 $\bigcirc$  Select "high-p\_" tracks (>2 GeV) by correlating hits in 2 nearby sensors (stub)



- In the end-cap, it depends on the location of the detector
  - ➡ End-cap configuration typically requires wider spacing (up to ~ 4 mm)

 $\Delta z = \Delta R / tg 9$ 





## CMS 2S modules







## 2S module prototype









## 2S module prototype











## Electronics







## **CMS PS modules**



#### P(ixel)S(strip) module $\bigcirc$ strips = 100 $\mu$ m x 2.4 cm $\bigcirc$ pixels = 100 $\mu$ m x 1.5 mm • Pixels are logically OR-ed for finding coincidence in the r- $\phi$ plane, and the precise z-coordinate is retained in the pixel storage and provided to the power converter trigger processors. 2 W concentrator 200 mW 2 x 8 SSA chips 512 mW L1 Data Pipeline @ BX (40 MHz) GBT & Pixel/Strip Memory & @ L1 (1 MHz) 2 x 8 MPA opto package Up to 60 bit Data compression 12 bits 3004 mW 800 mW each row Output @160 MHz Interface To the **Trigger Logic for Stub** @ BX (40 MHz) **SSA** @ BX (40 MHz) Concentrator Finding Up to 80 bit chip concentrator 200 mW Trigger Logic



## Data organization and dispatch



Solution Example CMS:  $8(r-\phi)x6(r-z)$  trigger sectors (some 10% overlapping)

Each sector ~200 stubs on average; tails up to ~500 stubs/event in 140 evts pileup+ttbar (to be compared with ATLAS-Phase 1 ~2000)

About 600 Gb/s per one trigger tower



Send data to Track-finding processors

#### Full mesh ATCA shelfs

- Capable of "40G" full-mesh backplane on 14 slots = 7.2 Tb/s
- Several options being investigated, all include time multiplexing data transfer from a set of receiving processors boards to pattern recognition and track finding engines
- O(10) time multiplexed at the shelf level

• keep latency < 5  $\mu$ s, including pattern recognition and track fitting

Number of connections to trigger processors







## CMS: toward a demonstrator







Each board is capable to receive data from up to 48 modules at 3.25Gbps, with total 156 Gbps per Board/RTM. 8 boards can receive up to 384 modules (one trigger tower worth)

The input data is then divided into 4 time slices, each slice is sent to 1 of 4 Pattern Recognition board, with 40Gbps full-mesh (4x40=160Gbps > 156Gbps).

Each Pattern Recognition board receives  $\underline{up}$  to 8 x 40Gbps = 320 Gbps input data over full Mesh backplane. The events can then be time multiplexed on board for each mezzanine to handle (x1, x2, x4 possible, flexible). Each board send out its output from RTM to next stage for each time slice. Also communicate with other boards in other crates for data sharing in phi & eta for each time slice



## **Tower interconnections**







40G full-mesh backplane



## Pattern recognition and Track fit



Associative Memories (pattern recognition) + FPGA (track fit)

- ©CMS trigger sectors need ~1M patterns: only 8 state of the art AM06-chip
  - Higher I/O speed (currently 2Gb/s/layer) to reduce time multiplexing
- - Alternative methods under study (Hough Transform, Retina)
- Search Alternative approaches under study

Purely FPGA based





## FPGA -only based L1 Track finding



# Find track seeds by correlating stubs in subsequent layers

- Serve where the same approach of "Kalman Filter"
  - Propagate stubs from one layer to another and concatenate stubs within matching windows
  - Linearized Track Fitter
  - Ghost removal





#### Pattern matching in CDF





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A pattern



#### $\rightarrow$ Superstrip definition:



 $\rightarrow$  A superstrip is simply a bunch of strips in one module of the tracking detector.

 $\rightarrow$  The superstrip address is the info sent to the AM board. Is is coded on a certain number of bits, depending on the superstrip resolution.

Superstrip enco	oding	Generic superstrip address definition			
Z module	Z inner	φ module	¢ inner		
Z segment (1bit) Z module (5bits) I of or 3.2mm pitch (5bits)					
	<u>↓</u> ↓	V	¥		
2 4 8 16	32 2 2 4	8 2 4	8 16 32 64		
	∲ module	(3bits) ∳ for 1.	6mm pitch (6bits)		

 $\rightarrow$  The encoding is divided into 4 parts, giving module and intra-module SS position in Z and  $\phi$  direction (*R is not necessary*)

 $\rightarrow$  We are not using pixel info yet, so our Z intramodule encoding is very basic for the moment.



## Two step approach



1. Find low resolution track candidates called "roads". Solve most of the pattern recognition



**FPGA** 



## Two step approach



1. Find low resolution track candidates called "roads". Solve most of the pattern recognition

 Then fit tracks inside roads.
 Thanks to 1<sup>st</sup> step it is much easier



## The AM chip at work



The event hit positions are received over 8 input buses of 15 bits each. All the hits are then compared with the data stored inside each layer block, as soon as they are loaded into the chip, each one in the corresponding bus. If a layer block is matched, the corresponding Flip-Flop (FF) is set. It should be noted that each hit is fed into the memory only once. In fact the bus line transmits the information to all the layer blocks, and, if matched, all the corresponding FF are set simultaneously. Finally, a given pattern is matched with a logic that counts the number of FF set to 1 within a row, using a majority logic: that means that one could ask a minimum number of FF set



## AM chips history



Several versions of the AMChip:

	Vers.	Design	Tech.	Area	Patterns	Package	Year	
ĺ	1	Full custom	700 nm		128	QFP	1992	
	2	FPGA	350 nm		128	QFP		
	3	Std cells	180 nm	100 mm <sup>2</sup>	5 k	QFP	2000	
	4	Std cells $+$	65 nm	14 mm <sup>2</sup>	8 k	QFP		
		Full custom						
	mini-5	Std cells +		<b>4 mm</b> <sup>2</sup>	0,5 k	QFP		
		Full custom	65 nm				2014	
	5	+ SERDES	05 1111	<b>12 mm</b> <sup>2</sup>	3 k	BGA		
		IP blocks						
	6	Std cells +	65 nm	<b>150 mm</b> <sup>2</sup>	128 k	BGA	2015	
		Full custom						
		+ SERDES		150 1111				
		IP blocks						

**red** = under fabrication

**blue** = under design (figures are estimated)

## AM technological evolution



XILINX<sup>®</sup>

3200E<sup>™</sup> 156AFS0025

- (90's) Full custom VLSI chip 0.7μm (INFN-Pisa)
- 128 patterns, 6x12bit words each, 30MHz
- F. Morsani et al., IEEE Trans. on Nucl. Sci., vol. 39 (1992)

Alternative FPGA implementation of SVT AM chip

P. Giannetti et al., Nucl. Intsr. and Meth., vol. A413/2-3, (1998)

G Magazzù, 1st std cell project presented @ LHCC (1999)



Standard Cell 0.18 µm → 5000 pattern/AM chip SVT upgrade total: 6M pattern, 40MHz A. Annovi et al., IEEE TNS, Vol 53, Issue 4, Part 2, 2006





A. Annovi -2013

14th ICATPP Conference



## Usage in ATLAS @L1.5



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# Track fitting - high quality helix parameters and $\chi^2$



#### Principal component analysis

Over a narrow region in the detector, equations linear in the local silicon hit coordinates give resolution nearly as good as a time-consuming helical fit.

Nucl.Instrum.Meth.A623:540-542,2010 doi:10.1016/j.nima.2010.03.063

$$\boldsymbol{p}_i = \sum_{j=1}^{14} \boldsymbol{a}_{ij} \boldsymbol{x}_j + \boldsymbol{b}_i$$



•pi's are the helix parameters and 2 components.

 $\cdot x_j$ 's are the hit coordinates in the silicon layers.

•a<sub>ij</sub> & b<sub>i</sub> are pre-stored constants determined from full simulation or real data tracks.

•The range of the linear fit is a "sector" which consists of a single silicon module in each detector layer.

•This is VERY fast in FPGA DSPs.

#### $\bigcirc$ ~3000 fitting engines/trigger sector for CMS





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First Hough transform made with a coarse binning (simple and fast process to identify accretion)



### Retina



- → Ristori, An artificial retina for fast track finding, Nucl. Instrum. Meth. A 453 (2000) 425
- **1** A track is described by  $n \leq 5$  parameters  $(p_T, \phi, \ldots)$
- Ø Build a n-dimensional grid with cell indexes i, j, ...
- **8** Each cell of this space represents 1 single track k with given parameters  $p_{T,i}$ ,  $\phi_j$ , ...
- For each event, **fill** each cell with this *weight*:

$$R_{ij\ldots} = \sum_{m} e^{-\frac{s_{ij\ldots,m}^2}{2\sigma^2}}$$
(1)

where

$$s_{ij\ldots,m} = x_m - x_k(p_{T,i}, \phi_j, \dots)$$
(2)

is the distance between the hit m and the track k (on the hit layer)

- **5** The sum is on every hit of the event
- 6  $\sigma$  is an adjustable parameter
- The weight is higher if the parametrized track is closer to the hits









- 1 If a weight is high for a given cell, a track with the parameters corresponding to that cell has produced the measured hits
- e Find maxima on the grid
- **8** Each maximum is a track
- **4** Interpolate between near cells to improve resolution

# First implementation in CMS on-going Some encouraging initial studies



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# Expected performances (tracklet)



- L1 tracking efficiency as function of  $\eta \& p_T$  for single  $\mu$ ,  $\pi$ , e with <PU>=140
  - Muons Sharp turn-on at 2 GeV & high efficiency across all  $\eta$
  - Pions Somewhat lower efficiency due to higher interaction rate
  - Electrons Slower turn-on curve, efficiency reduced from bremsstrahlung
- For  $|\eta|<$  1.0 & p\_T > 2 GeV, efficiency for  $\mu,\,\pi,\,e$  is >99%, 95%, 87%







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## Expected performances (Hough)



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## Usage of L1 Tracks - CMS





Matching Drift Tube trigger primitives with L1Tracks: large rate reduction:

> 10 at threshold > ~ 14 GeV. Normalized to present trigger at 10 GeV. Removes flattening at high  $P_t$ 



Rate reduction brought by matching L1 e/ $\gamma$  to L1Track stubs for  $|\eta| < 1$ . Red: with current (5x5 xtal) L1Cal granularity. Green : using single crystal-level position resolution improves matching



#### Primary Vertex & Track MET Primary Vertex and Track MET



- L1 tracks can also be used to reconstruct primary vertex of event
- Resolution of primary vertex using L1 tracks with  $p_T > 2 \text{ GeV}$  or 5 GeV
  - <1mm for events with large track multiplicity</p>
    - Here: ttbar <PU>=140
  - Similar performance with the higher track p<sub>T</sub> threshold

- Track "MET"
  - Define L1 track-based missing transverse momentum from L1 tracks coming from primary vertex





# SUSY Signal Point



- Rate reductions using L1 tracks for SUSY signal
  - Stop pair production with hadronic top decays (stop=775 GeV, LSP=550 GeV)
  - Signal defined by genMET > 100 GeV



- Missing H<sub>T</sub> determined with/without vertex association
  - Algo1 & Algo2: Calorimeter-based L1 jet algorithms with different PU subtraction methods
- Sizable rate reductions achieved with tracking information!







Preliminary simulation studies demonstrate addition of L1 tracking trigger provides significant gains in rate reduction with good efficiency for physics objects. Note these results are "work in progress".

Trigger, Threshold	Algorithm	Rate reduction	Full eff. at the plateau	Comments
Single Muon, 20 GeV	Improved Pt, via track matching	~ 13 (  η  < 1 )	~ 90 %	Tracker isolation may help further.
Single Electron, 20 GeV	Match with cluster	> 6 (current granularity) >10 (crystal granularity) (   η   < 1 )	90 %	Tracker isolation can bring an additional factor of up to 2.
Single Tau, 40 GeV	CaloTau – track matching + tracker isolation	O(5)	O(50 %) (for 3-prong decays)	
Single Photon, 20 GeV	Tracker isolation	40 %	90 %	Probably hard to do much better.
Multi-jets, HT	Require that jets come from the same vertex			Performances depend a lot on the trigger & threshold.





Tracker information helps reducing drastically the rate of uninterested events

- This will become a new "must" for all future detectors
- HL-LHC detectors will make use of tracking information in the Level-1 Triggers
  - Several trigger architectures exploited
    - ●Full readout @40 MHz, on-detector data reduction using p<sub>T</sub>-modules
    - Implications on Tracker detector layouts ongoing
  - Some demonstrators being built to validate the full chain
  - Large gains in combining tracking with other subdetectors
    ©Electrons, Muons, Jets and MET
  - High statistics of useful events for precision physics available
    Stay tuned!